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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/675,485	09/30/2003	Ajay Kwatra	16356.824 (DC-05255)	5996
27683	7590	03/16/2006	EXAMINER	
HAYNES AND BOONE, LLP 901 MAIN STREET, SUITE 3100 DALLAS, TX 75202			DU, THUAN N	
			ART UNIT	PAPER NUMBER

2116

DATE MAILED: 03/16/2006

Please find below and/or attached an Office communication concerning this application or proceeding.

Office Action Summary

Application No.

10/675,485

Applicant(s)

KWATRA, AJAY

Examiner

Thuan N. Du

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-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --

Period for Reply

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) OR THIRTY (30) DAYS, WHICHEVER IS LONGER, FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

Status

- 1) ☒ Responsive to communication(s) filed on 30 September 2003.
- 2a) ☐ This action is **FINAL**. 2b) ☒ This action is non-final.
- 3) ☐ Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

Disposition of Claims

- 4) ☒ Claim(s) 1-28 is/are pending in the application.
- 4a) Of the above claim(s) _____ is/are withdrawn from consideration.
- 5) ☐ Claim(s) _____ is/are allowed.
- 6) ☒ Claim(s) 1-28 is/are rejected.
- 7) ☐ Claim(s) _____ is/are objected to.
- 8) ☐ Claim(s) _____ are subject to restriction and/or election requirement.

Application Papers

- 9) ☐ The specification is objected to by the Examiner.
- 10) ☒ The drawing(s) filed on 26 January 2004 is/are: a) ☐ accepted or b) ☒ objected to by the Examiner.
Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).
Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).
- 11) ☐ The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.

Priority under 35 U.S.C. § 119

- 12) ☐ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
- a) ☐ All b) ☐ Some * c) ☐ None of:
1. ☐ Certified copies of the priority documents have been received.
 2. ☐ Certified copies of the priority documents have been received in Application No. _____.
 3. ☐ Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).

* See the attached detailed Office action for a list of the certified copies not received.

Attachment(s)

- | | |
|--|---|
| 1) <input checked="" type="checkbox"/> Notice of References Cited (PTO-892) | 4) <input type="checkbox"/> Interview Summary (PTO-413)
Paper No(s)/Mail Date. _____ |
| 2) <input type="checkbox"/> Notice of Draftsperson's Patent Drawing Review (PTO-948) | 5) <input type="checkbox"/> Notice of Informal Patent Application (PTO-152) |
| 3) <input checked="" type="checkbox"/> Information Disclosure Statement(s) (PTO-1449 or PTO/SB/08)
Paper No(s)/Mail Date <u>9/30/03</u> . | 6) <input type="checkbox"/> Other: _____ |

DETAILED ACTION

1. It is hereby acknowledged that the following papers have been received and placed of record in the file: Drawings (dated 1/26/04) and Preliminary Amendment (dated 10/1/04).
2. Claims 1-28 are presented for examination.

Drawings

3. The drawings are objected to under 37 CFR 1.83(a). The drawings must show every feature of the invention specified in the claims. Therefore, the parallel connection as claimed in claims 10, 24 and 27 must be shown or the feature(s) canceled from the claim(s). No new matter should be entered.

Corrected drawing sheets in compliance with 37 CFR 1.121(d) are required in reply to the Office action to avoid abandonment of the application. Any amended replacement drawing sheet should include all of the figures appearing on the immediate prior version of the sheet, even if only one figure is being amended. The figure or figure number of an amended drawing should not be labeled as "amended." If a drawing figure is to be canceled, the appropriate figure must be removed from the replacement sheet, and where necessary, the remaining figures must be renumbered and appropriate changes made to the brief description of the several views of the drawings for consistency. Additional replacement sheets may be necessary to show the renumbering of the remaining figures. Each drawing sheet submitted after the filing date of an application must be labeled in the top margin as either "Replacement Sheet" or "New Sheet" pursuant to 37 CFR 1.121(d). If the changes are not accepted by the examiner, the applicant will

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be notified and informed of any required corrective action in the next Office action. The objection to the drawings will not be held in abeyance.

Claim Rejections - 35 USC § 112

4. The following is a quotation of the second paragraph of 35 U.S.C. 112:

The specification shall conclude with one or more claims particularly pointing out and distinctly claiming the subject matter which the applicant regards as his invention.

5. Claims 1-28 are rejected under 35 U.S.C. 112, second paragraph, as being indefinite for failing to particularly point out and distinctly claim the subject matter which applicant regards as the invention.

6. Regarding claims 1, 15, 28 and 28, it is not clear “an SMBus” refers to what type of bus in the system.

7. Regarding claims 6, 7, 20 and 21, “standard” changes over time, hence, it is inappropriate to have the scope of a claim change with time. Since organizations implementing standards meet regularly and have the authority to modify standards, any connection a claim may have to these standards may vary over time. The other aspect arising from this is enablement. If the standard changes, the disclosure may no longer support the limitation.

8. Claims 2-5, 8-14, 16-19 and 22-27 are also rejected for incorporating the above deficiency by dependency.

Claim Rejections - 35 USC § 103

9. The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:

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(a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negated by the manner in which the invention was made.

10. Claims 1-28 are rejected under 35 U.S.C. 103(a) as being unpatentable over Harari et al. [Harari] (U.S. Patent No. 6,981,068) and Larson et al. [Larson] (U.S. Patent No. 6,968,470).

11. Regarding claims 1, 5-7 and 10, Harari teaches a card device (mother card 10) operable to transfer power from a first subsystem (host) to a second subsystem (daughter card) of a computer, the card device comprising:

a first port electrically coupled to the first subsystem by a first connector, wherein the first connector includes at least one pin terminal to transfer the power and at least one pin terminal coupled to a SMBus of the computer (the standard 68-pin connector of a PC card includes power pin terminal and data pin terminal) [col. 7, lines 8-14];

a second port electrically coupled to the second subsystem by a second connector, wherein the second connector includes at least one pin terminal to transfer the power [col. 7, lines 15-17];

a transfer component (58) electrically coupled to the first and second ports, wherein the transfer component is operable to transfer the power [col. 7, lines 61-63; col. 7, line 65 to col. 8, line 2]; and

a memory component electrically coupled to the SMBus [col. 7, lines 57-61].

Harari does not teach the memory component storing an identifier to uniquely identify the device, wherein an amount of the power received by the device is adjusted in response to the identifier.

Larson teaches a card device comprising a memory component coupled to a system bus, the memory storing an identifier to uniquely identify the device, wherein an amount of the power received by the device is adjusted in response to the identifier [col. 10, lines 42-47].

It would have been obvious to one of ordinary skill in the art at the time of the invention was made to modify the teachings of Harari to include the identifier embedded in the card device as taught by Larson. The modification would increase the accuracy of providing power to the card device.

12. Regarding claims 2 and 4, Harari teaches that the first subsystem is a host (computer). Therefore, a card controller inherently included in the host.

Harari teaches that the card device is a removable card device. Therefore, one of ordinary skill in the art would have recognized that the host (first subsystem) would include a detection component for detecting the present/absent of the card device.

13. Regarding claim 3, Larson teaches that the identifier is read [col. 10, lines 52-55].

14. Regarding claim 8, Harari teaches that the first connector includes 28 pins [col. 7, lines 8-10].

15. Regarding claim 9, Harari teaches the standard 68-pin connector of a PC card includes power pin terminal and data pin terminal.

16. Regarding claims 11-24, since they recite method of operating of the apparatus defined in the apparatus claims, they are rejected accordingly based on the rejection of the apparatus claims.

17. Regarding claims 25-28, in addition to the rejection of claim 1 above, Harari teaches that:

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a first subsystem, wherein the first subsystem is defined to include: a processor, a system bus, and a memory coupled to the processor through the system bus (a host computer inherently includes those component);

a second subsystem, wherein the second subsystem is defined to include at least one expansion card (daughter card).

Conclusion

18. Any inquiry concerning this communication or earlier communications from the examiner should be directed to Thuan N. Du whose telephone number is (571) 272-3673. The examiner can normally be reached on Monday-Friday: 9:30 AM - 6:00 PM, EST.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Lynne H. Browne can be reached on (571) 272-3670.

Central TC telephone number is (571) 272-2100.

The fax number for the organization is (571) 273-8300.

19. Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll free).

TD
March 13, 2006


THUAN N. DU
PRIMARY EXAMINER